**SPECTRUM**
SYSTEMENTWICKLUNG MICROELECTRONIC GMBH

M2i.61xx - 8 bit 125 MS/s Arbitrary Waveform Generator

- **Fast 8 bit arbitrary waveform generator**
- **1, 2 or 4 channel versions**
- **Simultaneously generation on all channels**
- **Output up to ± 3 V in 50 Ohm**
- **Amplifier option available for ±10 V**
- **Offset and amplitude programmable**
- **3 software selectable filters**
- **Up to 2 GSamples on-board memory**
- **512 MSample standard memory installed**
- **FIFO mode continuous streaming output**
- **Synchronization option available for up to 16 cards**
- **Features: Multiple Replay, Gated Replay, BaseXIO, Amplifier**
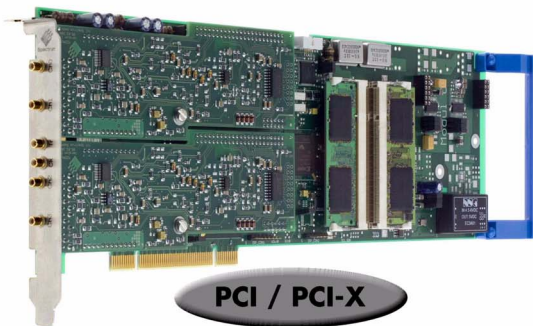
PCI / PCI-X
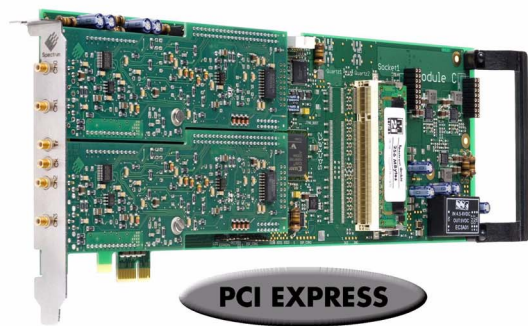
PCI EXPRESS

- 66 MHz 32 bit PCI-X interface
- 5V / 3.3V PCI compatible
- 100% compatible to conventional PCI > V2.1
- Sustained streaming mode up to 245 MB/s

- 2,5 GBit x1 PCIe Interface
- Works with x1/x4/x8/x16* PCIe slots
- Software compatible to PCI
- Sustained streaming mode up to 160 MB/s

Operating Systems	Recomended Software	Drivers and Examples
• Windows XP, Vista, 7, 8, 10 • Linux Kernel 2.4, 2.6, 3.x, 4.x • Windows/Linux 32 and 64 bit	• SBench 6 • MATLAB • LabVIEW, LabWindows/CVI	• Visual Basic, C/C++, GNU C+ • Borland Delphi, .VB.NET, C#, J# • Python

Model	1 channel	2 channels	4 channels
M2i.6105	125 MS/s		
M2i.6110	125 MS/s	125 MS/s	
M2i.6111	125 MS/s	125 MS/s	125 MS/s

General Information

The M2i.61xx series offers 3 different versions of arbitrary waveform generators for the PCI/PCI-X bus and PCI Express. With these boards it is possible to generate free definable waveforms on several channels synchronously. There are up to four channels on one board with a maximum update rate of 125 MS/s. With one of the synchronization options the setup of synchronous multi channel systems is possible. It is also possible to combine the arbitrary waveform generator with other boards of the M2i product family like analog acquisition or digital I/O boards.

With the up to 2 GSample large on-board memory long waveforms can be generated even with high sampling rates. The M2i.61xx series offers 3 different versions of arbitrary. All boards of the M2i.61xx series may use the whole installed on-board memory completely for the currently activated number of channels. The memory can also be used as a FIFO buffer to make continuously data transfer from PC memory or hard disk.

*Some x16 PCIe slots are for the use of graphic cards only and can not be used for other cards.

Software Support

Windows drivers

The cards are delivered with drivers for Windows XP, as well as Vista, Windows 7 and Windows 8 (each 32 bit and 64 bit). Programming examples for Visual C++, Borland C++ Builder, LabWindows/CVI, Borland Delphi, Visual Basic, VB.NET, C#, J# and Python are included.

Linux Drivers

All cards are delivered with full Linux support. Pre compiled kernel modules are included for the most common distributions like RedHat, Fedora, Suse, Ubuntu LTS or Debian. The Linux support includes SMP systems, 32 bit and 64 bit systems, versatile programming examples for Gnu C++ as well as the possibility to get the driver sources for your own compilation.

SPEasyGenerator

For a fast start with the hardware the simple signal generation software SPEasyGenerator is included in the delivery. This software allows to generator simple signal shapes like sine, triangle or rectangle with programmable frequency, amplitude and phase. SPEasy-Generator is also available as LabWindows source code.

SBench

A full licence of SBench the easy-to-use graphical operating software for the Spectrum cards is included in the delivery. The version 6 is running under Windows as well as under Linux (KDE and GNOME).

Third-party products

Spectrum supports the most popular third-party software products such as LabVIEW, MATLAB or LabWindows/CVI. All drivers come with detailed documentation and working examples are included in the delivery. Support for other software packages, like VEE or DasyLab, can also be provided on request.

Hardware features and options

PCI/PCI-X

The cards with PCI/PCI-X bus connector use 32 Bit and up to 66 MHz clock rate for data transfer. They are 100% compatible to Conventional PCI > V2.1. The universal interface allows the use in PCI slots with 5 V I/O and 3.3 V I/O voltages as well as in PCI-X or PCI 64 slots. The maximum sustained data transfer rate is 245 MByte/s per bus segment.

PCI Express

The cards with PCI Express use a x1 PCIe connector. They can be used in PCI Express x1/x4/x8/x16 slots, except special graphic card slots, and are 100% software compatible to Conventional PCI > V2.1. The maximum sustained data transfer rate is 160 MByte/s per slot.

Singleshot output

When singleshot output is activated the data of the on-board memory is replayed exactly one time. The trigger source can be either the external TTL trigger or the software trigger.

Repeated output

When the repeated output mode is used the data of the on-board memory is replayed continuously until a stop command is executed or N times. The trigger source can be either the external TTL trigger or the software trigger.

FIFO mode

The FIFO mode is designed for continuous data transfer between measurement board and PC memory (up to 245 MB/s on a PCI-X slot, up to 125 MB/s on a PCI slot and up to 160 MB/s on a PCIe slot) or hard disk. The control of the data stream is done automatically by the driver on interrupt request. The complete installed on-board memory is used for buffer data, making the continuous streaming extremely reliable.

Single Restart replay

When this mode is activated the data of the on-board memory will be replayed once after each trigger event. The trigger source can be either the external TTL trigger or software trigger.
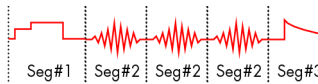
Multiple Replay
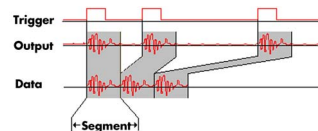
The Multiple Replay mode allows the fast repetition output on several trigger events without restarting the hardware. With this option very fast repetition rates can be achieved. The on-board memory is divided into several segments of the same size. Each segment then generates an output with the occurrence of each trigger event.

Sequence Mode

The sequence mode allows to split the card memory into several data segments of different length. These data segements are chained up in a user chosen order using an additional sequence memory. In this sequence memory the number of loops for each segment can be programmed and trigger conditions can be defined to proceed from segment to segment. Using the sequence mode it is also possible to switch between replay waveforms by a simple software command or to redefine waveform data for segments simultaneously while other segments are being replayed.

Gated Replay
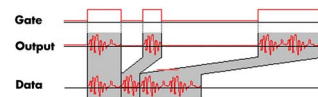
The Gated Sampling mode allows data replay controlled by an external gate signal. Data is only replayed if the gate signal has attained a programmed level.

External clock I/O

Using a dedicated line a sampling clock can be fed in from an external system. It's also possible to output the internally used sampling clock to synchronize external equipment to this clock.

Reference clock
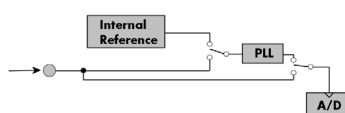
The option to use a precise external reference clock (normally 10 MHz) is necessary to synchronize the instrument for high-quality

measurements with external equipment (like a signal source). It's also possible to enhance the quality of the sampling clock in this way. The driver automatically generates the requested sampling clock from the fed in reference clock.

±10 V Amplifier

 The amplifier board allows the output of ±10 V on up to four channels without software modification. The standard outputs of the card are amplified by factor 3.33. The amplifier which has 30 MHz bandwidth has an output impedance of 50 Ohm. This allows ±10 V with high impedance termination or ±5 V with 50 ohm termination.
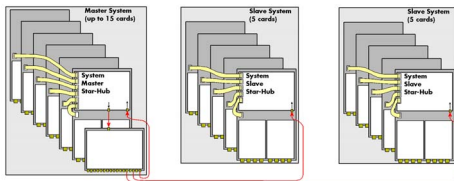
Star-Hub

 The star-hub is an additional module allowing the phase stable synchronisation of up to 16 boards in one system. Independent of the number of boards there is no phase delay between all channels. The star-hub distributes trigger and clock information between all boards. As a result all connected boards are running with the same clock and the same trigger. All trigger sources can be combined with OR/AND allowing all channels of all cards to be trigger source at the same time. The star-hub is available as 5 card and 16 card version. The 5 card version doesn't need an extra slot.

271 synchronous cards with theSystem Star-Hub

 With the help of multiple system star-hubs it is possible to link up to 17 system phase synchronous with each other. Each system can then contain up to 16 cards (master only 15). In total 271 cards can be used fully synchronously in a bunch of systems. One master system distributes clock and trigger signal to all connected slave systems.

BaseXIO (enhanced trigger)

 The BaseXIO option offers 8 asynchronous digital I/O lines on the base card. The direction can be selected by software in groups of four. Two of these lines can also be used as additional external trigger sources. This allows the building of complex trigger conjunctions with external gated triggers as well as AND/OR conjunction of multiple external trigger sources like, for example, the picture and row synchronisation of video signals. In addition one of the I/O lines can be used as reference clock for the Timestamp counter.

Technical Data

Analog Outputs

Resolution		8 bit
Output amplitude	software programmable	±100 mV up to ±3 V in 1 mV steps (Amp option: ±333 mV up to ±10 V in 3 mV steps)
Output offset	software programmable	±3 V in 1 mV steps (Amp option: ±10 V in 3 mV steps)
Filters	software programmable	no filter or one of 3 different filters as defined in technical data section
DAC Differential non linearity (DNL)	DAC only	±1.5 LSB typical
DAC Integral non linearity (INL)	DAC only	±1.0 LSB typical
Output resistance		< 1 Ohm
Minimum output load		35 Ohm (not short circuit protected)
Max output swing in 50 Ohm		± 3 V (offset + amplitude)
Max slew rate (no filter)		> 0.9 V/ns
Crosstalk @ 1 MHz signal ±3 V		< -80 dB
Output accuracy		< 1%

Trigger

Running mode	software programmable	Singleshot, FIFO mode (Streaming), Repeated Replay, Single Restart, Sequence Mode
Trigger edge	software programmable	Rising edge, falling edge or both edges
Trigger pulse width	software programmable	0 to [64k - 1] samples in steps of 1 sample
Trigger delay	software programmable	0 to [64k - 1] samples in steps of 1 sample
Memory depth	software programmable	8 up to [installed memory / number of active channels] samples in steps of 4
Multiple Replay segment size	software programmable	8 up to [installed memory / 2 / active channels] samples in steps of 4
Multiple Replay, Gated Replay: re-arming time		< 4 samples
Trigger output delay		One positive edge after internal trigger event
Internal/External trigger accuracy		1 sample
External trigger type (input and output)		3.3V LVTTL compatible (5V tolerant)
External trigger input		Low ≤ 0.8 V, High ≥ 2.0 V, ≥ 8 ns in pulse stretch mode, ≥ 2 clock periods all other modes
External trigger maximum voltage		-0.5 V up to +5.7 V (internally clamped to 5.0V, 100 mA max. clamping current)
Trigger impedance	software programmable	50 Ohm / high impedance (> 4kOhm)
External trigger output type		3.3 V LVTTL
External trigger output levels		Low ≤ 0.4 V, High ≥ 2.4 V, TTL compatible
External trigger output drive strength		Capable of driving 50 ohm load, maximum drive strength ±128 mA
Output delay trigger to 1st sample		15/16 clocks (2/1 channel/module)
Gate end to last replayed sample		15/16 clocks (2/1 channel/module)
Gate end alignment		2 samples (1 ch), 1 sample (2 or 4 ch)

Clock

Clock Modes	software programmable	internal PLL, internal quartz, external clock, external divided, external reference clock, sync
Internal clock range (PLL mode)	software programmable	1 kS/s to max using internal reference, 50kS/s to max using external reference clock
Internal clock accuracy		≤ 20 ppm
Internal clock setup granularity		≤1% of range (100M, 10M, 1M, 100k,...): Examples: range 1M to 10M: stepsize ≤ 100k
External reference clock range	software programmable	≥ 1.0 MHz and ≤ 125.0 MHz
External clock impedance	software programmable	50 Ohm / high impedance (> 4kOhm)
External clock range		see „Dynamic Parameters" table below
External clock delay to internal clock		5.4 ns
External clock type/edge		3.3V LVTTL compatible, rising edge used
External clock input		Low level ≤ 0.8 V, High level ≥ 2.0 V, duty cycle: 45% - 55%
External clock maximum voltage		-0.5 V up to +3.8 V (internally clamped to 3.3V, 100 mA max. clamping current)
External clock output type		3.3 V LVTTL
External clock output levels		Low ≤ 0.4 V, High ≥ 2.4 V, TTL compatible
External clock output drive strength		Capable of driving 50 ohm load, maximum drive strength ±128 mA
Synchronization clock divider	software programmable	2 up to [8k - 2] in steps of 2
ABA mode clock divider for slow clock	software programmable	8 up to 524280 in steps of 8

Clock and Filter and Dynamic Parameters

	M2i.6110 M2i.6105	M2i.6111
max internal/external clock	125 MS/s	125 MS/s
min internal clock	1kS/s	1kS/s
min external clock	DC	DC
-3 dB bandwidth no filter	> 60 MHz	> 60 MHz
Filter 3: Characteristics	5th order Butterworth	
Filter 3: -3 dB bandwidth	25 MHz (typ. 26.5 MHz)	25 MHz (typ. 26.5 MHz)
Filter 2: Characteristics	4th order Butterworth	
Filter 2: -3 dB bandwidth	5 MHz (typ. 5.8 MHz)	5 MHz (typ. 5.8 MHz)
Filter 1: Characteristics	4th order Butterworth	
Filter 1: -3 dB bandwidth	500 kHz (typ. 495 kHz)	500 kHz (typ. 495 kHz)

		M2i.61xx
Test - Samplerate	Test - Samplerate	125 MS/s
Output Frequency	Output Frequency	400 kHz
Output Level	Output Level	±2 V
Used Filter	Used Filter	500 kHz
SNR (typ)	SNR (typ)	> 60.9 dB
THD (typ)	THD (typ)	< -69.1 dB
SFDR (typ), excl harm.	SFDR (typ), excl harm.	> 71.9 dB

Dynamic parameters are measured at the given output level and 50 Ohm termination with a high resolution data acquisition card and are calculated from the spectrum. The sample rate that is selected is the maximum possible one. All available channels are activated for the tests. SNR and SFDR figures may differ depending on the quality of the used PC. SNR = Signal to Noise Ratio, THD = Total Harmonic Distortion, SFDR = Spurious Free Dynamic Range

Sequence Replay Mode

Number of sequence steps	software programmable	1 up to 512 (sequence steps can be overloaded at runtime)
Number of memory segments	software programmable	2 up to 256 (segment data can be overloaded at runtime)
Loop Count	software programmable	1 to 1M loops
Sequence Step Commands	software programmable	Loop for #Loops, Next, Loop until Trigger, End Sequence
Special Commands	software programmable	Data Overload at runtime, sequence steps overload at runtime

BaseXIO Option

BaseXIO modes	software programmable	Asynch digital I/O, 2 additional trigger, timestamp reference clock, timestamp digital inputs
BaseXIO direction	software programmable	Each 4 lines can be programmed in direction
BaseXIO input		TTL compatible: Low ≤ 0.8 V, High ≥ 2.0 V
BaseXIO input impedance		4.7 kOhm towards 3.3 V
BaseXIO input maximum voltage		-0.5 V up to +5.5 V
BaseXIO output type		3.3 V LVTLL
BaseXIO output levels		TTL compatible: Low ≤ 0.4 V, High ≥ 2.4 V
BaseXIO output drive strength		32 mA maximum current, no 50 Ω loads

±10 V Amplifier Card Options

Bandwidth	30 MHz
Max. input voltage	±3 V
Output impedance	50 Ω
Fixed Amplification	x 3.3
Max. Output Voltage (into high impedance load)	±10 V
Max. Output Voltage (into 50 Ohm load)	±5 V
Analog ground to PC system ground impedance	10 kΩ (with ground jumper unplugged), 0 Ω (when ground jumper is plugged)
Gain Error	≤ ±1 %
Offset Error	≤ ±50 mV

PCIe Version M2i.6-Exp-1Amp/2Amp/4Amp

Interface	PCIe x1 (power connection only)
Dimension (PCB without SMB connectors)	147 mm x 106 mm
Power Consumption 3.3 V	0.0 A
Power Consumption 12.0 V	-1Amp and -2Amp: 1.1 A, -4Amp: 2.1 A

PCI Version MI.6xxxx-1Amp/2Amp/4Amp

Interface	PCI 32 Bit 33 MHz (power connection only)
Dimension (PCB without SMB connectors)	147 mm x 106 mm
Power Consumption 3.3 V	0.0 A
Power Consumption 5.0 V	-1Amp and -2Amp: 2.5 A, -4Amp: 5.0 A

Connectors

Analog Inputs		3 mm SMB male (one for each single-ended input)	Cable-Type: Cab-3f-xx-xx
Trigger Input/Output	programmable direction	3 mm SMB male (one connector)	Cable-Type: Cab-3f-xx-xx
Clock Input/Output	programmable direction	3 mm SMB male (one connector)	Cable-Type: Cab-3f-xx-xx
Option Digital Inputs/Outputs		40 pole half pitch (Hirose FX2 series)	Cable-Type: Cab-d40-xx-xx
Option BaseXIO		8 x 3 mm SMB male on extra bracket, internally 8 x MMCX female	

Environmental and Physical Details

Dimension (PCB only)	312 mm x 107 mm (full PCI length)
Width (Standard or with option star-hub 5)	1 full size slot
Width (star-hub 16)	additionally back of adjacent neighbour slots
Width (with option BaseXIO)	additionally extra bracket on neighbour slot
Width (with option -digin, -digout or -60xx-AmpMod)	additionally half length of adjacent neighbour slot
Weight (depending on version)	290g (smallest version) up to 460g (biggest version with all options, including star-hub)
Warm up time	10 minutes
Operating temperature	0°C to 50°C
Storage temperature	-10°C to 70°C
Humidity	10% to 90%

PCI/PCI-X specific details

PCI / PCI-X bus slot type	32 bit 33 MHz or 32 bit 66 MHz
PCI / PCI-X bus slot compatibility	32/64 bit, 33-133 MHz, 3,3 V and 5 V I/O

PCI Express specific details

PCIe slot type	x1 Generation 1
PCIe slot compatibility	x1/x4/x8/x16 (Some x16 PCIe slots are for graphic cards only and can not be used)

Certification, Compliance, Warranty

EMC Immunity	Compliant with CE Mark
EMC Emission	Compliant with CE Mark
Product warranty	2 years starting with the day of delivery
Software and firmware updates	Life-time, free of charge

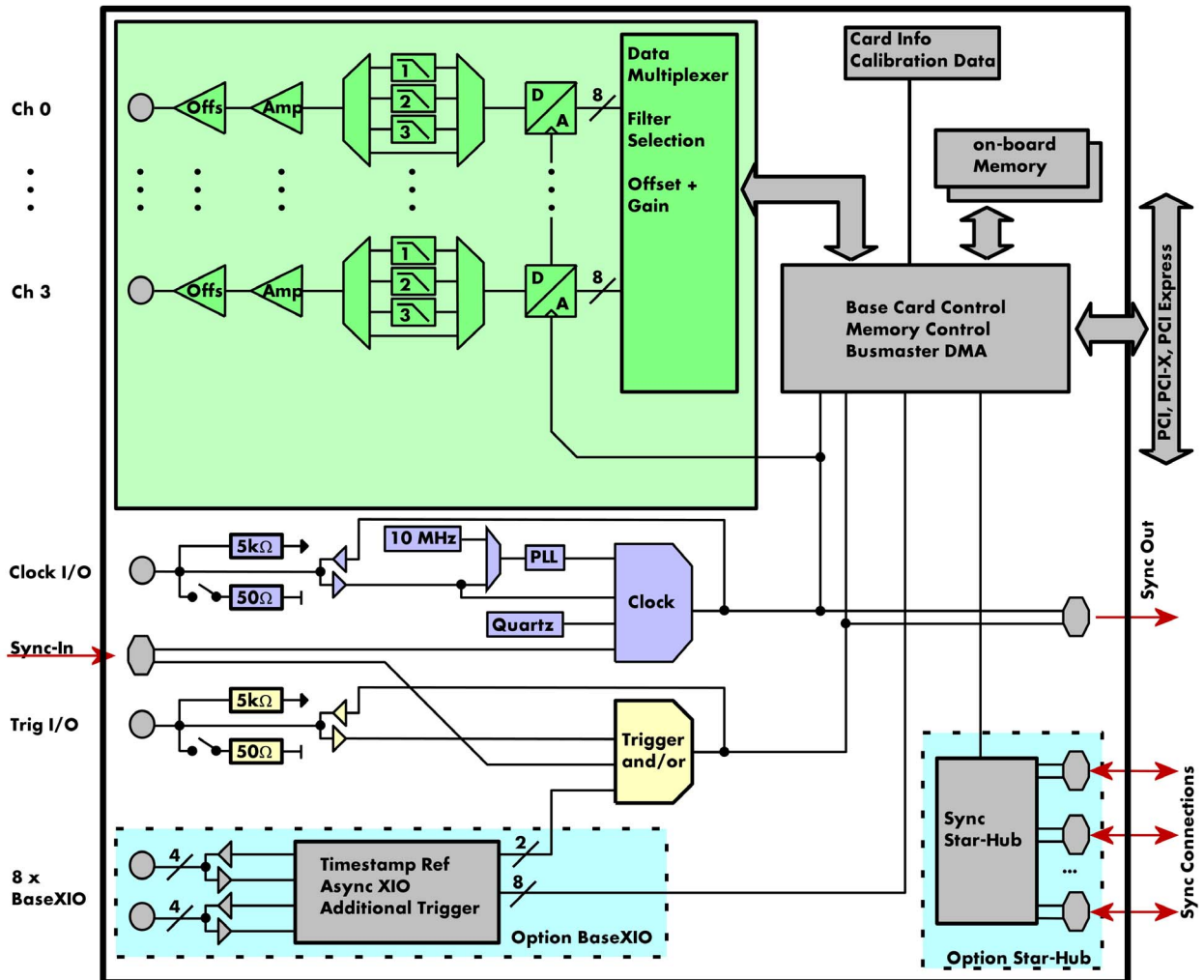## Power Consumption

	PCI / PCI-X			PCI EXPRESS		
	3.3 V	5 V	Total	3.3V	12V	Total
M2i.6105 (512 MS memory)	2.6 A	0.4 A	11.6 W	0.4 A	0.9 A	12.1 W
M2i.6110 (512 MS memory)	2.8 A	0.7 A	12.8 W	0.4 A	1.1 A	14.5 W
M2i.6111 (512 MS memory)	3.2 A	1.1 A	16.1 W	0.4 A	1.2 A	15.7 W
M2i.6111 (4 GS memory), max. power	4.9 A	1.1 A	21.7 W	0.4 A	1.7 A	21.7 W

MTBF

MTBF   200000 hours

Hardware block diagram

Order Information

The card is delivered with 512 MByte on-board memory and supports standard replay (single-shot, loop, single restart), FIFO replay (streaming), Multiple Replay, Gated Replay and Sequence Mode. Operating system drivers for Windows/Linux 32 bit and 64 bit, examples for C/C++, LabVIEW (Windows), MATLAB (Windows and Linux), LabWindows/CVI, .NET, Delphi, Visual Basic, Python and a Base license of the oscilloscope software SBench 6 are included. Drivers for other 3rd party products like VEE or DASYLab may be available on request.

Adapter cables are not included. Please order separately!

PCI Express (PCIe) / PCI/PCI-X

PCI Express	PCI/PCI-X	Standard mem	1 channel	2 channels	4 channels
M2i.6105-exp	M2i.6105	512 MByte	125 MS/s		
M2i.6110-exp	M2i.6110	512 MByte	125 MS/s	125 MS/s	
M2i.6111-exp	M2i.6111	512 MByte	125 MS/s	125 MS/s	125 MS/s

Memory

Order no.	Option
M2i.xxxx-1GB	Memory upgrade to 1 GB of total memory
M2i.xxxx-2GB	Memory upgrade to 2 GB of total memory

Options

Order no.	Option
M2i.xxxx-SH5 (1)	Synchronization Star-Hub for up to 5 cards, only 1 slot width
M2i.xxxx-SH16 (1)	Synchronization Star-Hub for up to 16 cards
M2i.xxxx-SSHM (1)	System-Star-Hub Master for up to 15 cards in the system and up to 17 systems, PCI 32 Bit card, sync cables and extra bracket for clock and trigger distribution included
M2i.xxxx-SSHMe (1)	System-Star-Hub Master for up to 15 cards in the system and up to 17 systems, PCI Express card, sync cables and extra bracket for clock and trigger distribution included
M2i.xxxx-SSHS5 (1)	System-Star-Hub Slave for 5 cards in one system, one slot width all sync cables + bracket included
M2i.xxxx-SSHS16 (1)	System-Star-Hub Slave for 16 cards in system, two slots width, all sync cables + bracket included
MI.6xxx-1Amp	1 channel ±10 V output amplifier PCI 32 bit card including 15 cm SMB to SMB connection cable
MI.6xxx-2Amp	2 channel ±10 V output amplifier PCI 32 bit card including 15 cm SMB to SMB connection cables
MI.6xxx-4Amp	4 channel ±10 V output amplifier PCI 32 bit card including 15 cm SMB to SMB connection cables
M2i.6-exp-1Amp	1 channel ±10 V output amplifier PCIe x1 card including 15 cm SMB to SMB connection cable
M2i.6-exp-2Amp	2 channel ±10 V output amplifier PCIe x1 card including 15 cm SMB to SMB connection cables
M2i.6-exp-4Amp	4 channel ±10 V output amplifier PCIe x1 card including 15 cm SMB to SMB connection cables
M2i.xxxx-bxio	Option BaseXIO: 8 digital I/O lines usable as asynchronous I/O and additional external trigger lines, additional bracket with 8 SMB connectors
M2i-upgrade	Upgrade for M2i.xxxx: later installation of option -dig, -2DigM, -4DigM, -SH5, -SH16 or -bxio

Cables

for Connections	Length	Order no. to BNC male	to BNC female	to SMA male	to SMA female	to SMB female
Analog/Clock/Trigger	80 cm	Cab-3f-9m-80	Cab-3f-9f-80	Cab-3f-3mA-80	Cab-3f-3fA-80	Cab-3f-3f-80
Analog/Clock/Trigger	200 cm	Cab-3f-9m-200	Cab-3f-9f-200	Cab-3f-3mA-200	Cab-3f-3fA-200	Cab-3f-3f-200
Probes (short)	5 cm		Cab-3f-9f-5			

Software SBench6

Order no.	
SBench6	Base version included in delivery. Supports standard mode for one card.
SBench6-Pro	Professional version for one card: FIFO mode, export/import, calculation functions
SBench6-Multi	Option multiple cards: Needs SBench6-Pro. Handles multiple synchronized cards in one system.
Volume Licenses	Please ask Spectrum for details.

(1) : Just one of the options can be installed on a card at a time.
(2) : Third party product with warranty differing from our export conditions. No volume rebate possible.

Technical changes and printing errors possible